*Fig. 2*

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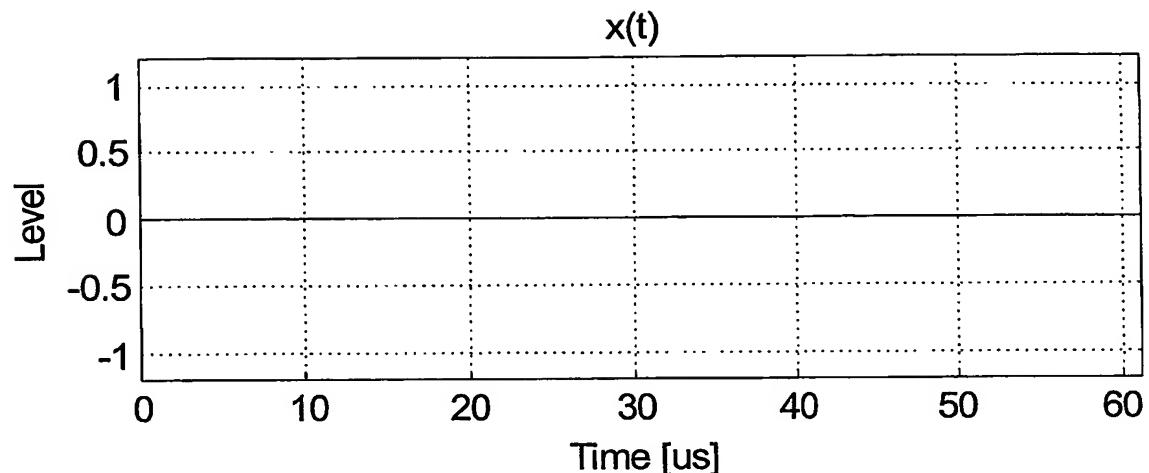


Fig. 3a

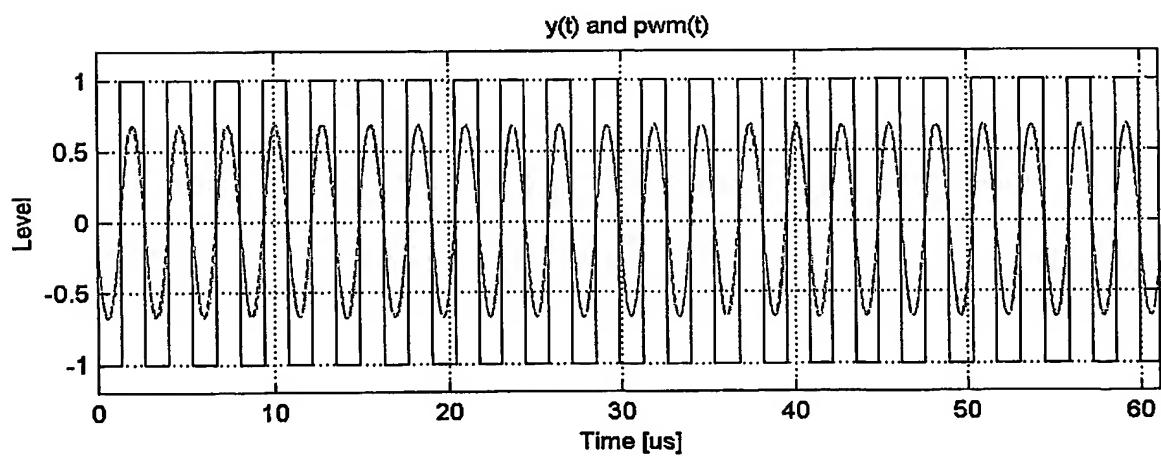


Fig. 3b

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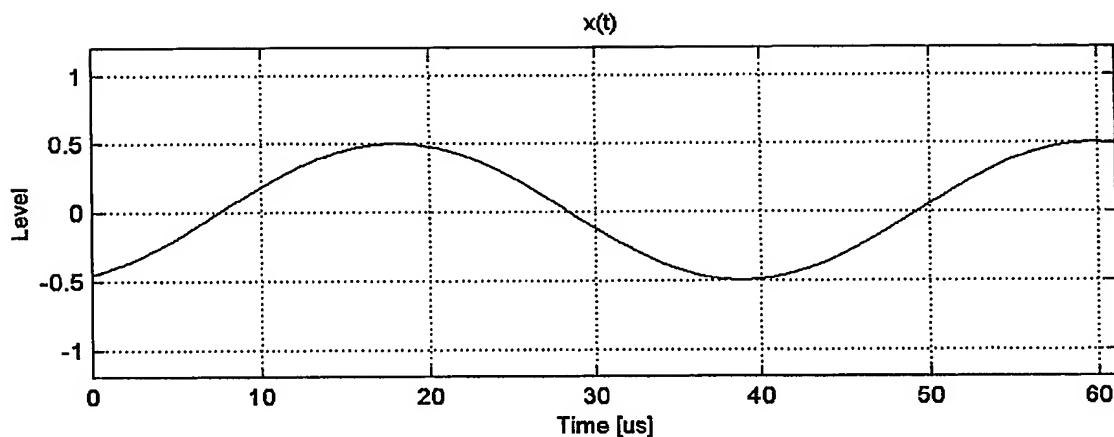


Fig. 4a

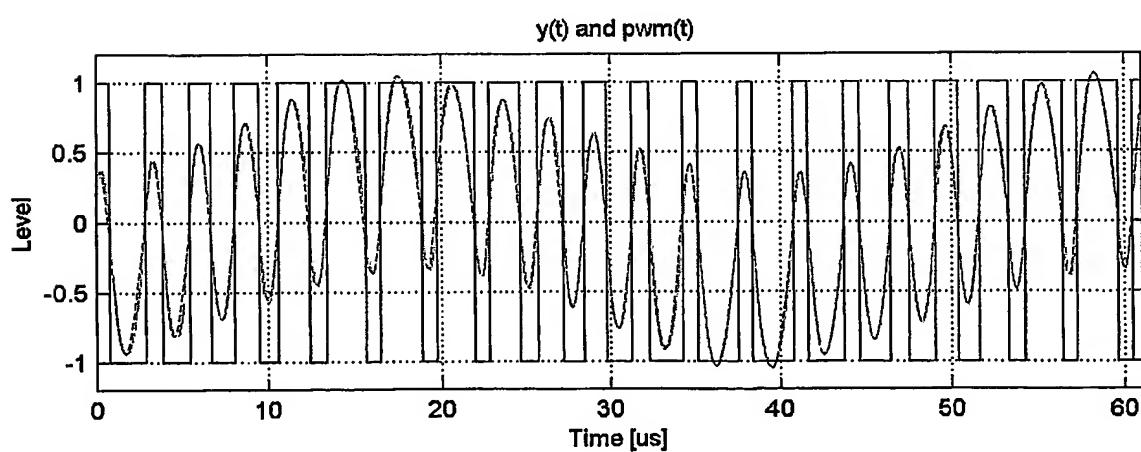


Fig. 4b

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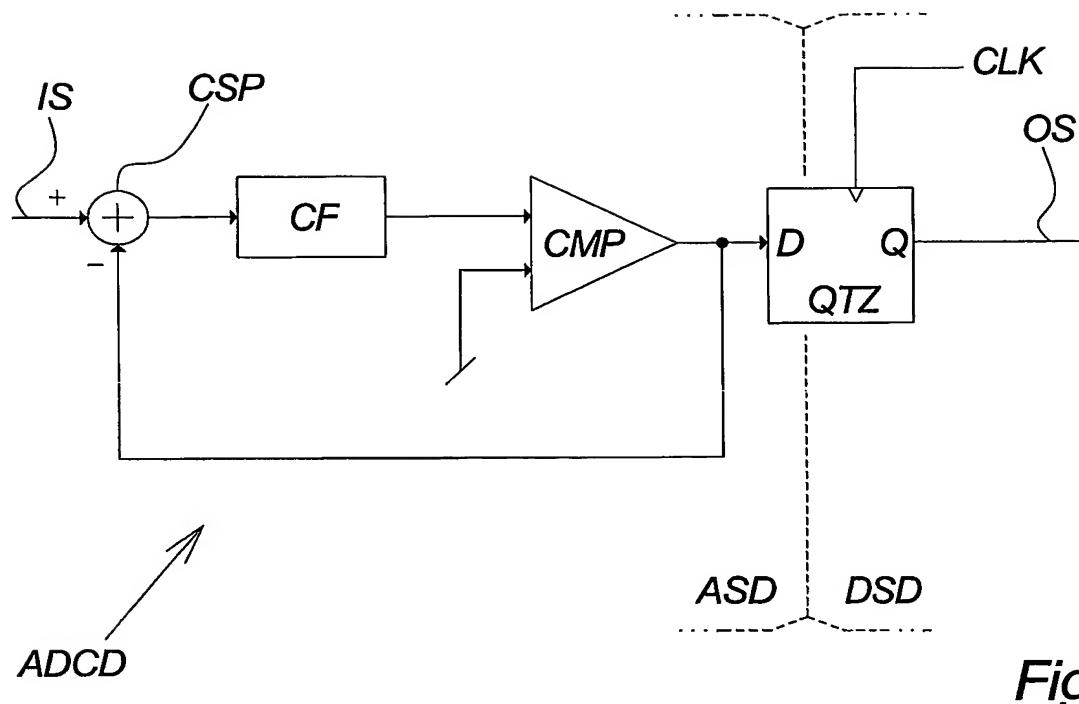


Fig. 5

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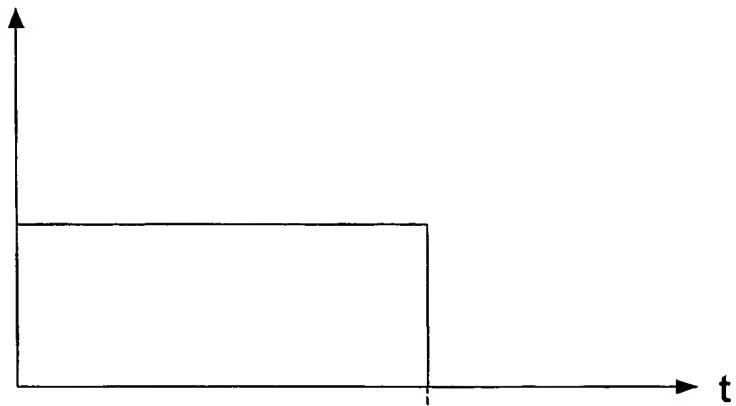


Fig. 6a

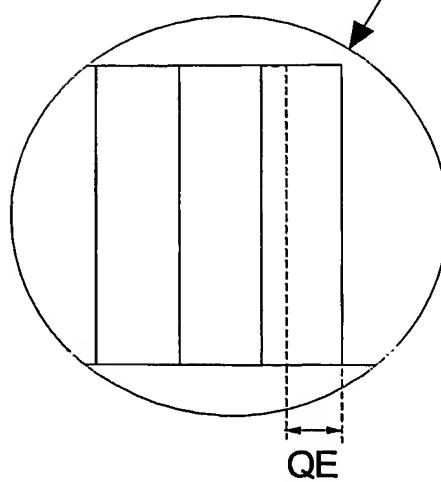
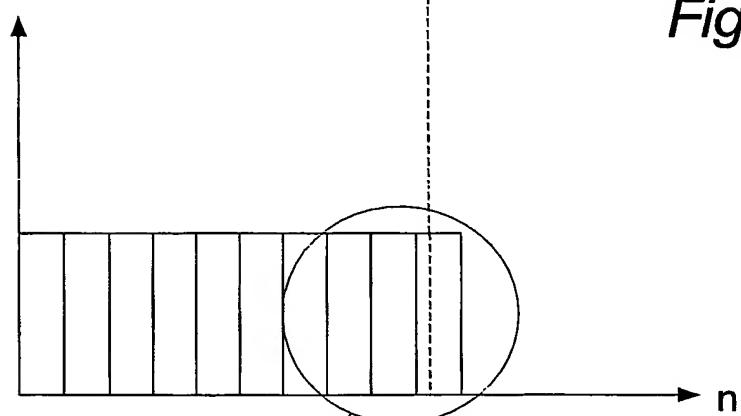


Fig. 6b

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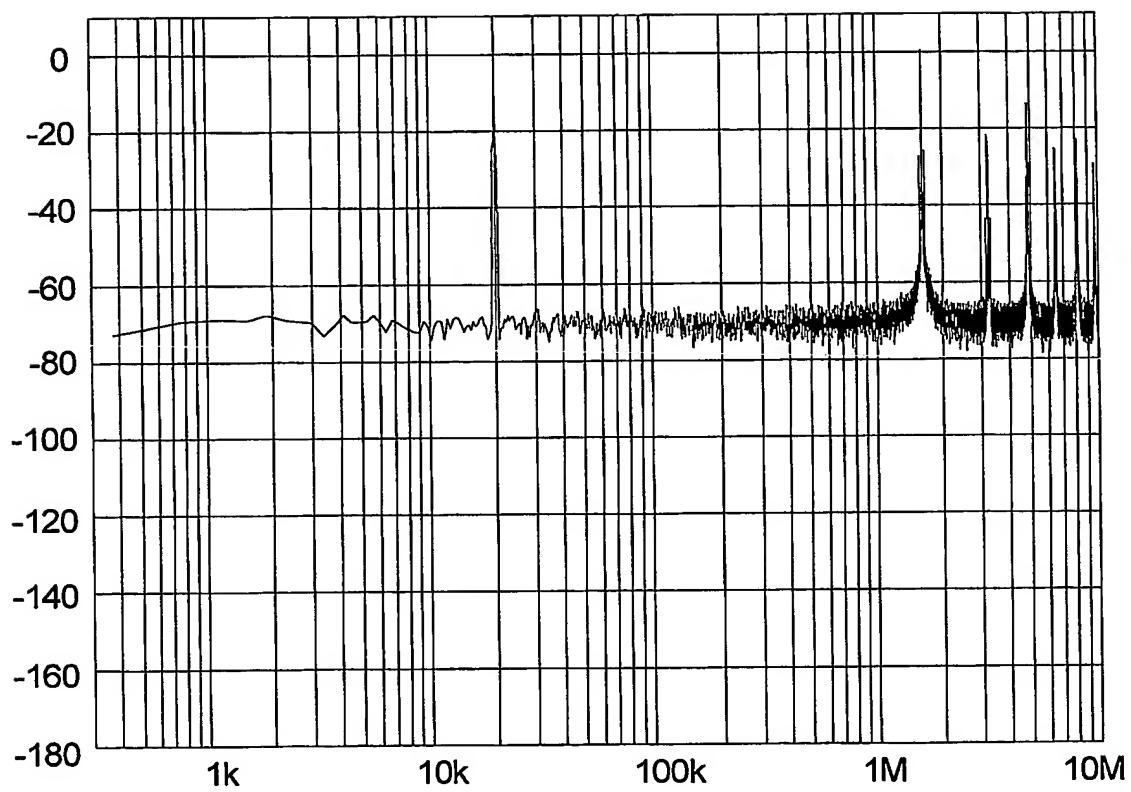


Fig. 7

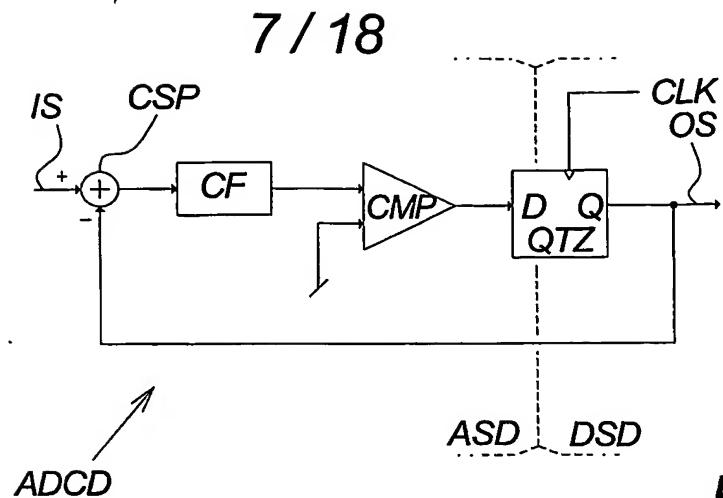


Fig. 8a

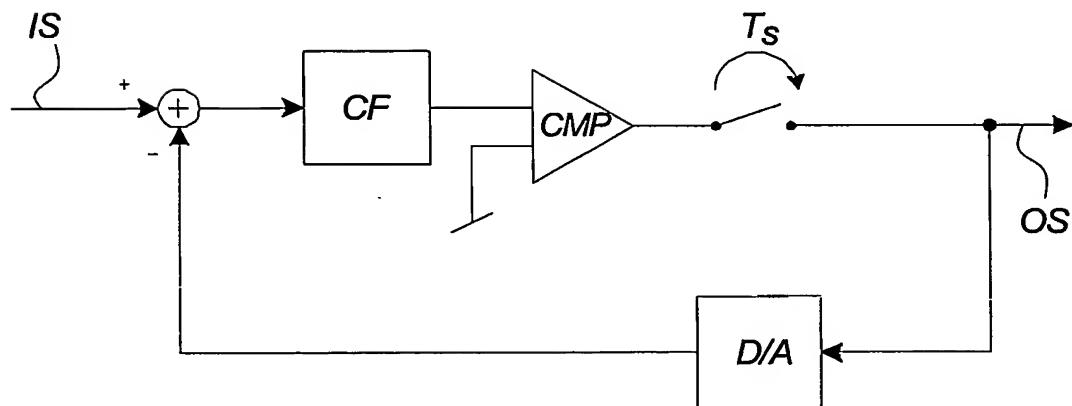
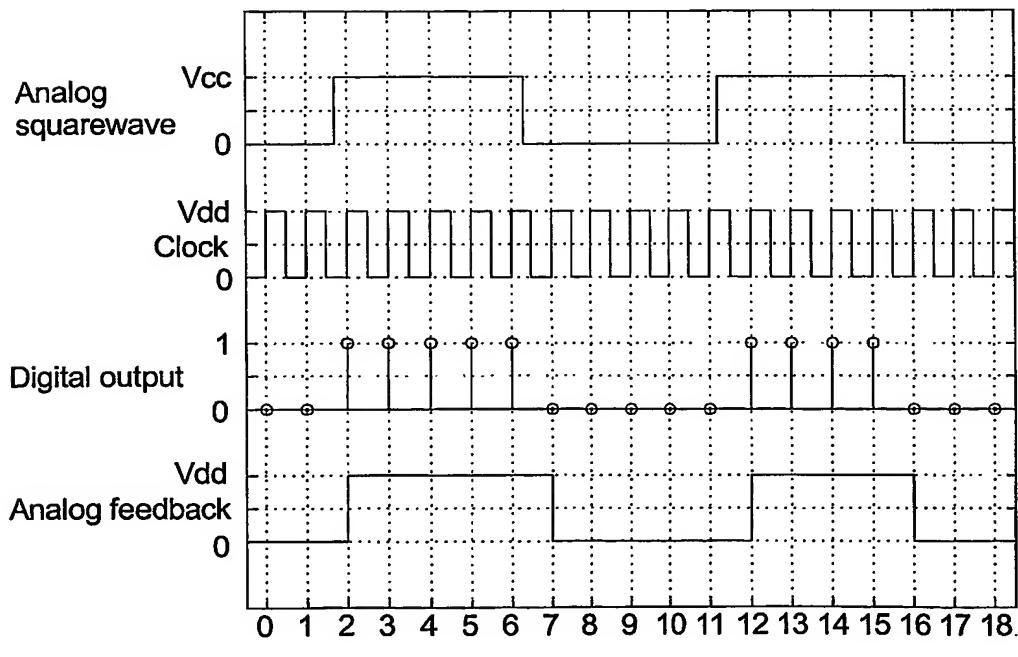


Fig. 8b



Time [clock periods]

Fig. 8c

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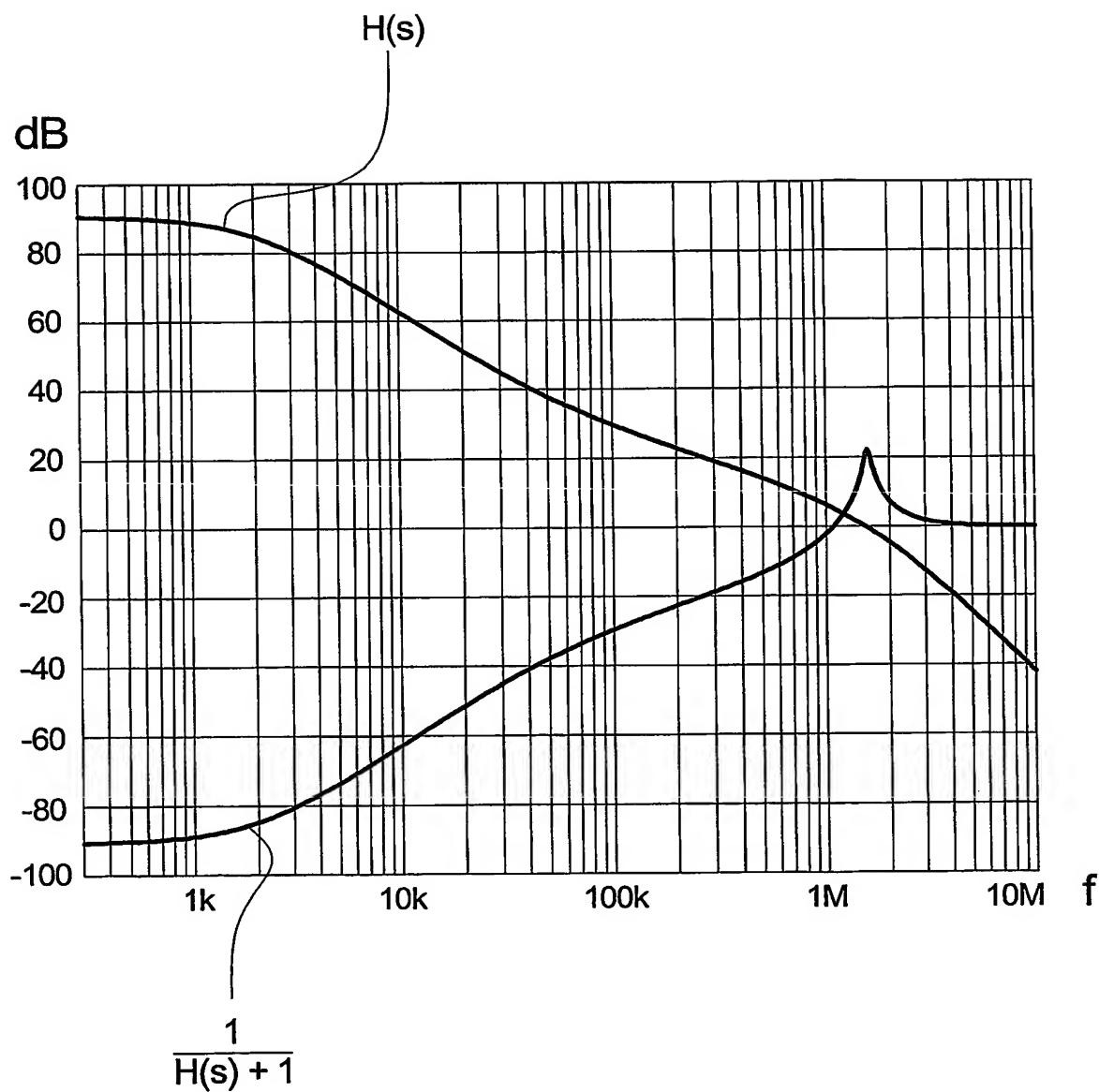


Fig. 9

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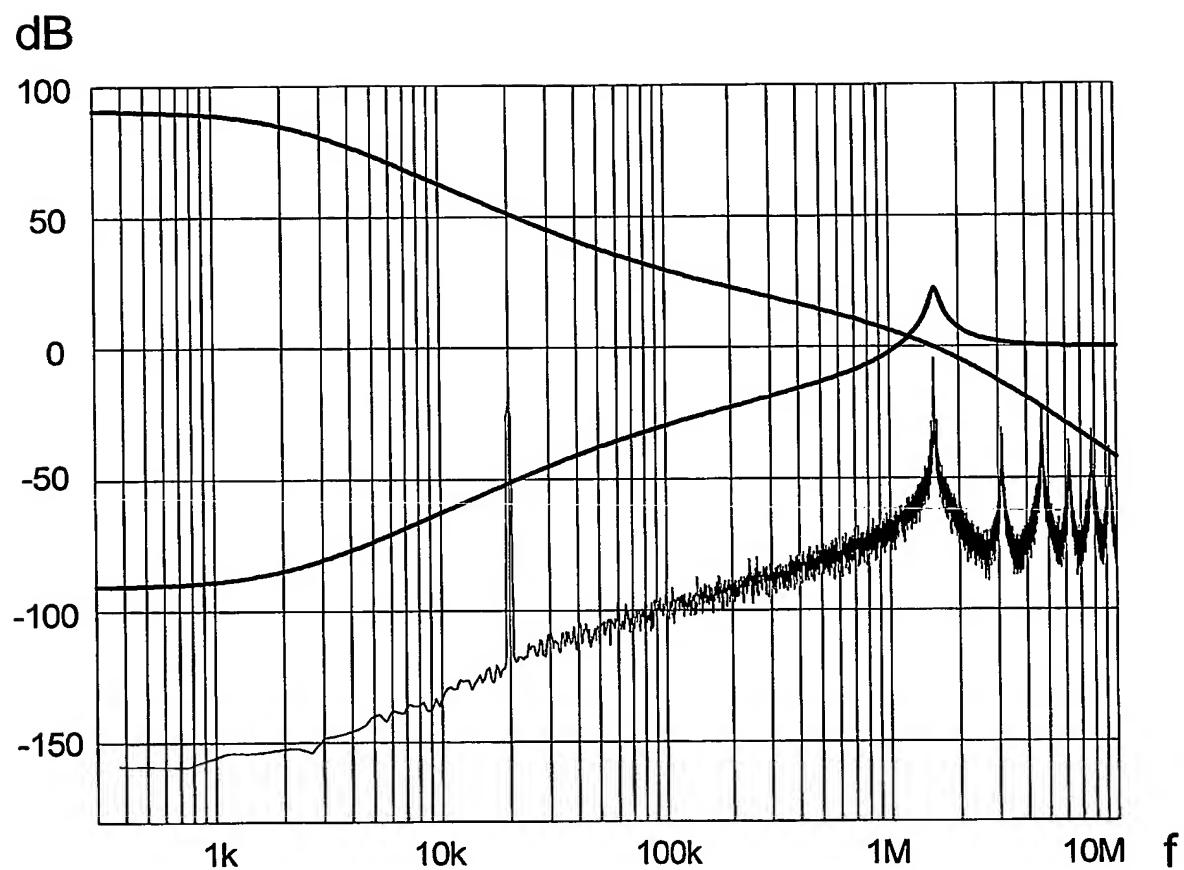


Fig. 10

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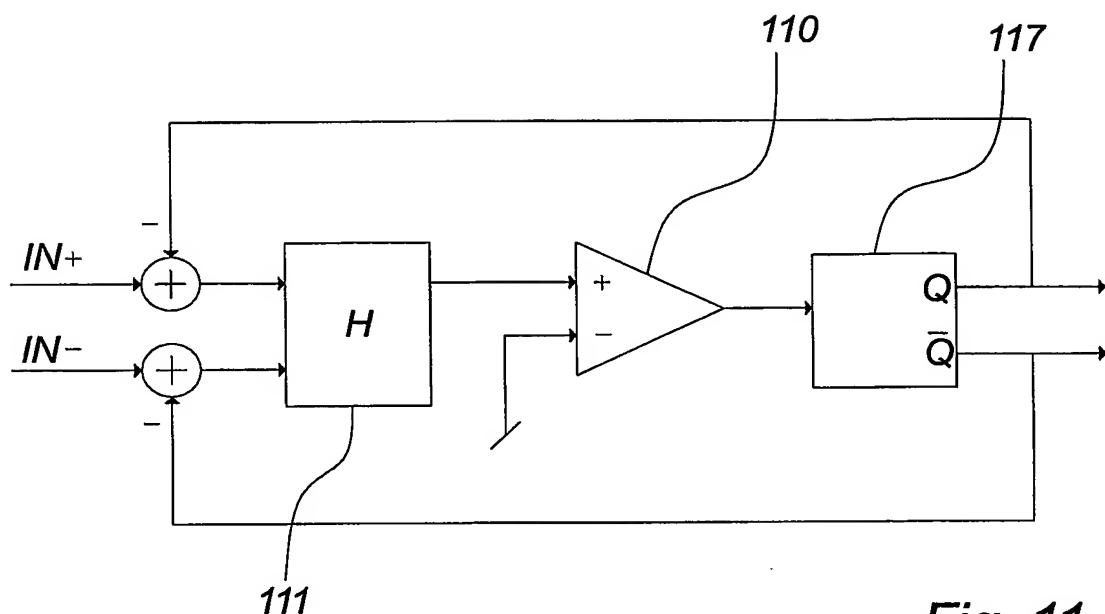


Fig. 11

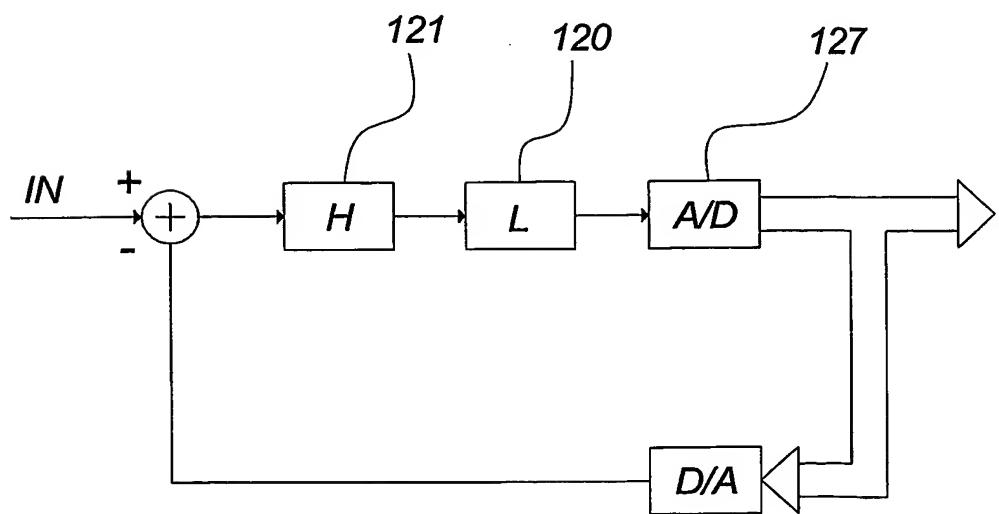


Fig. 12

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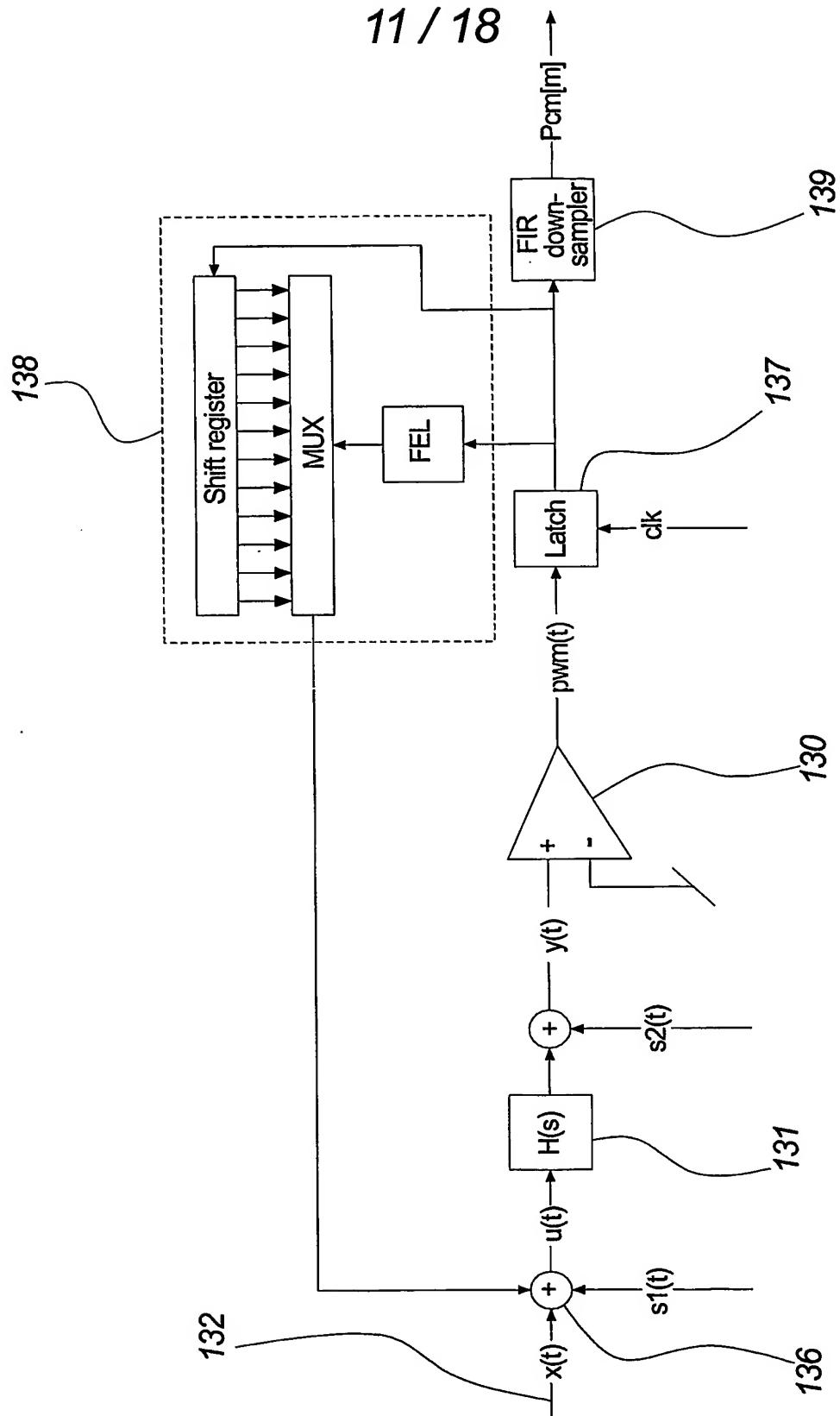


Fig. 13

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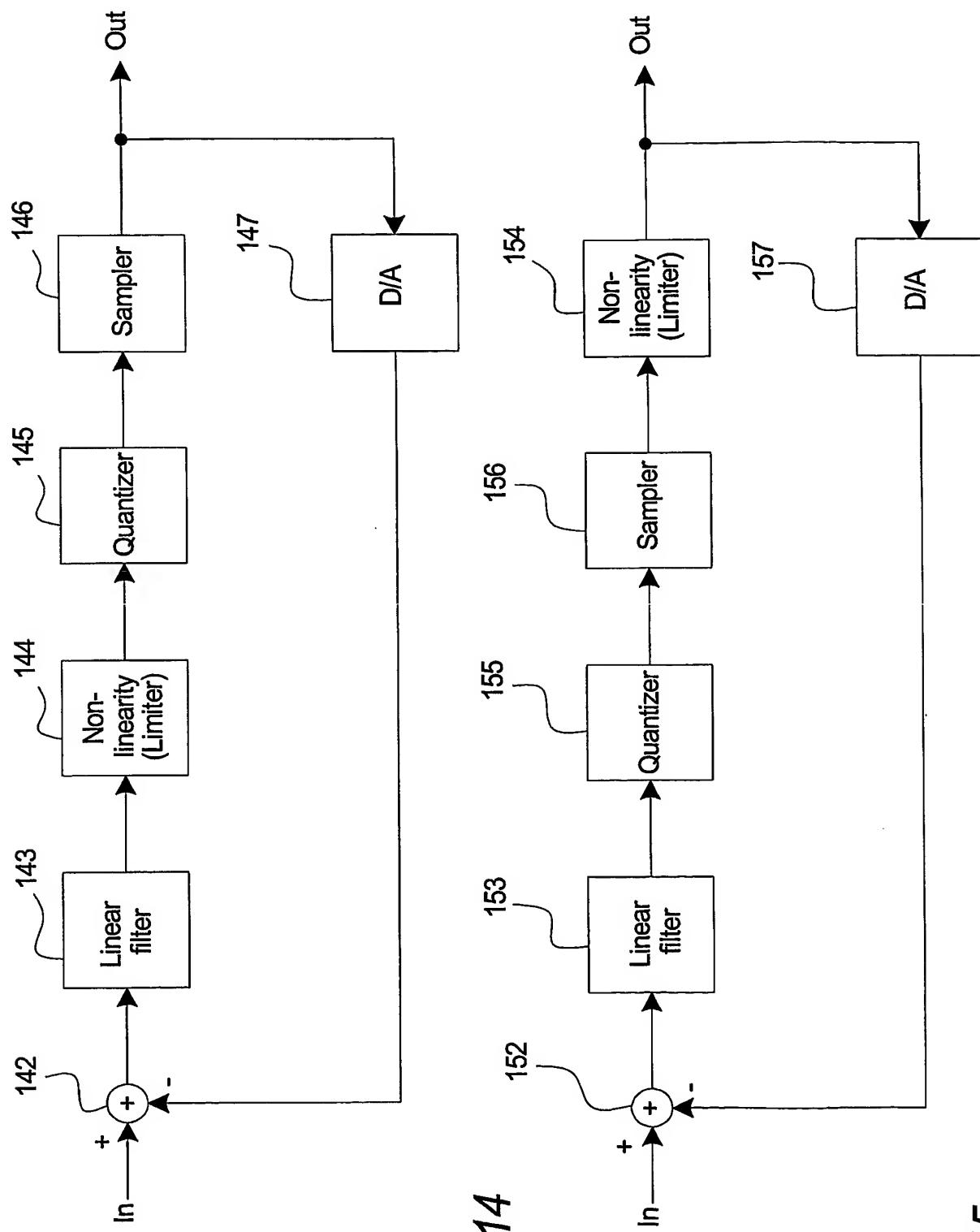


Fig. 14

Fig. 15

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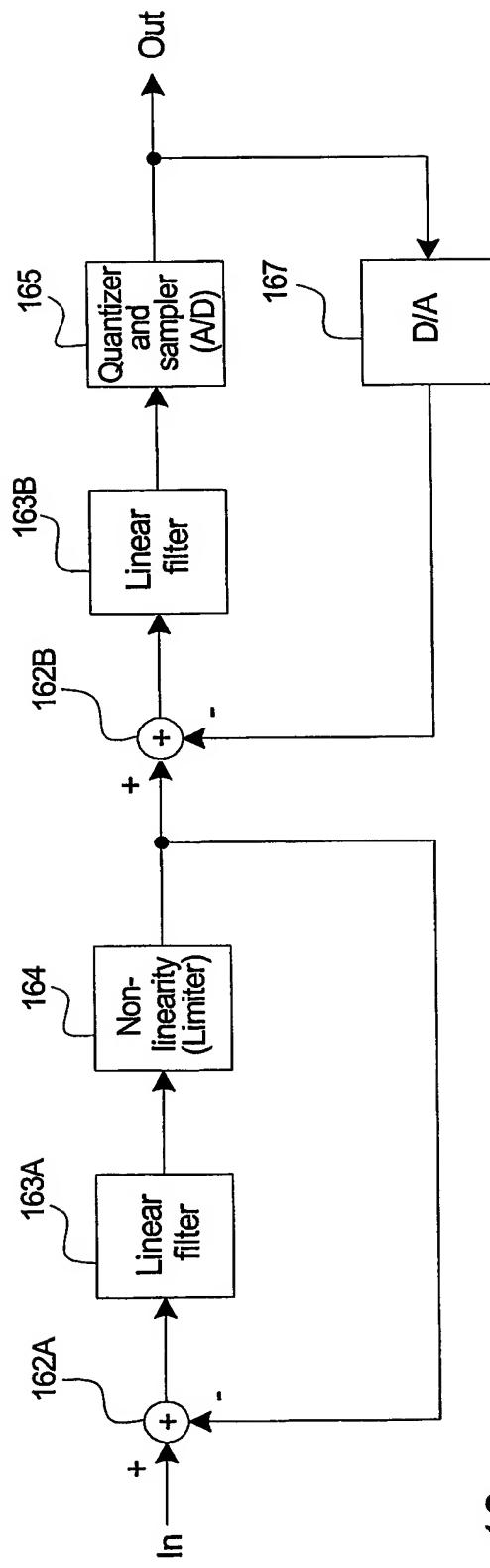


Fig. 16

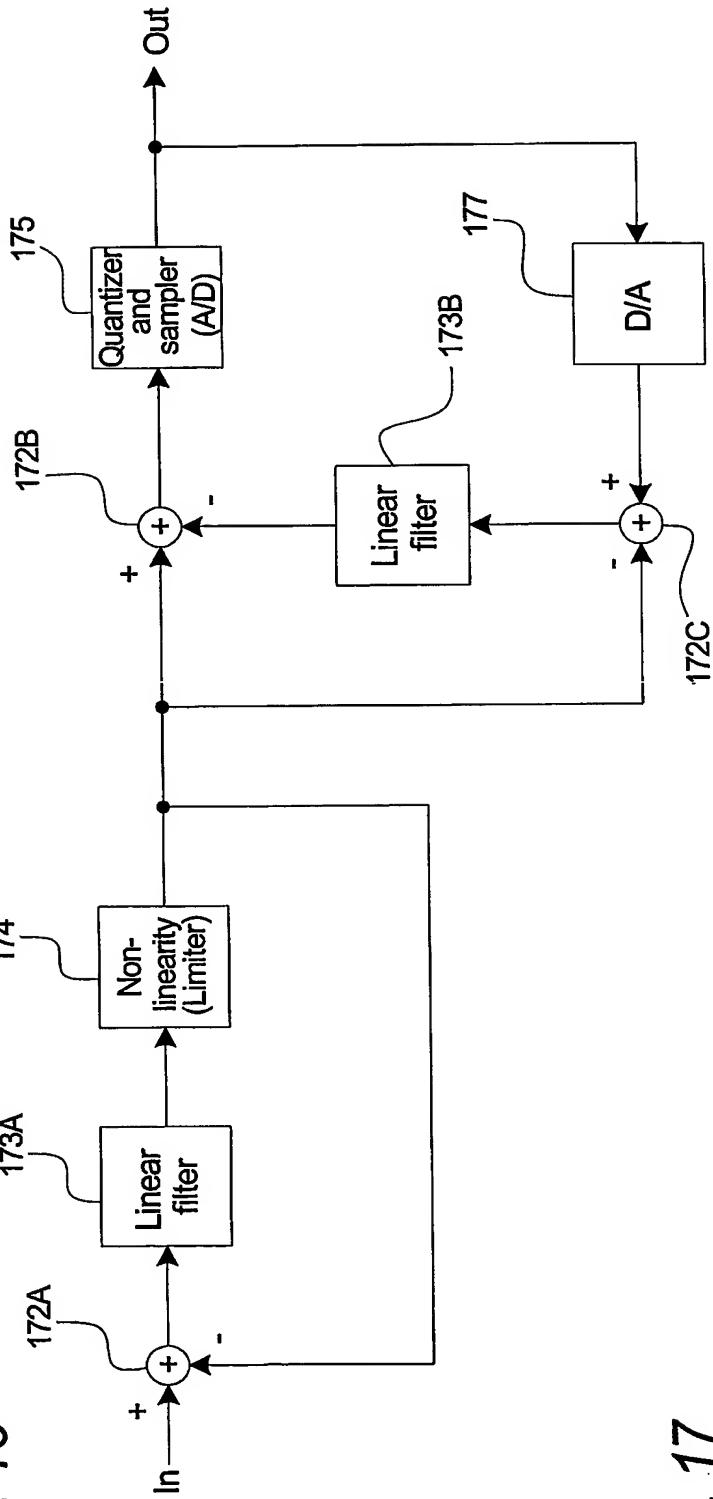


Fig. 17

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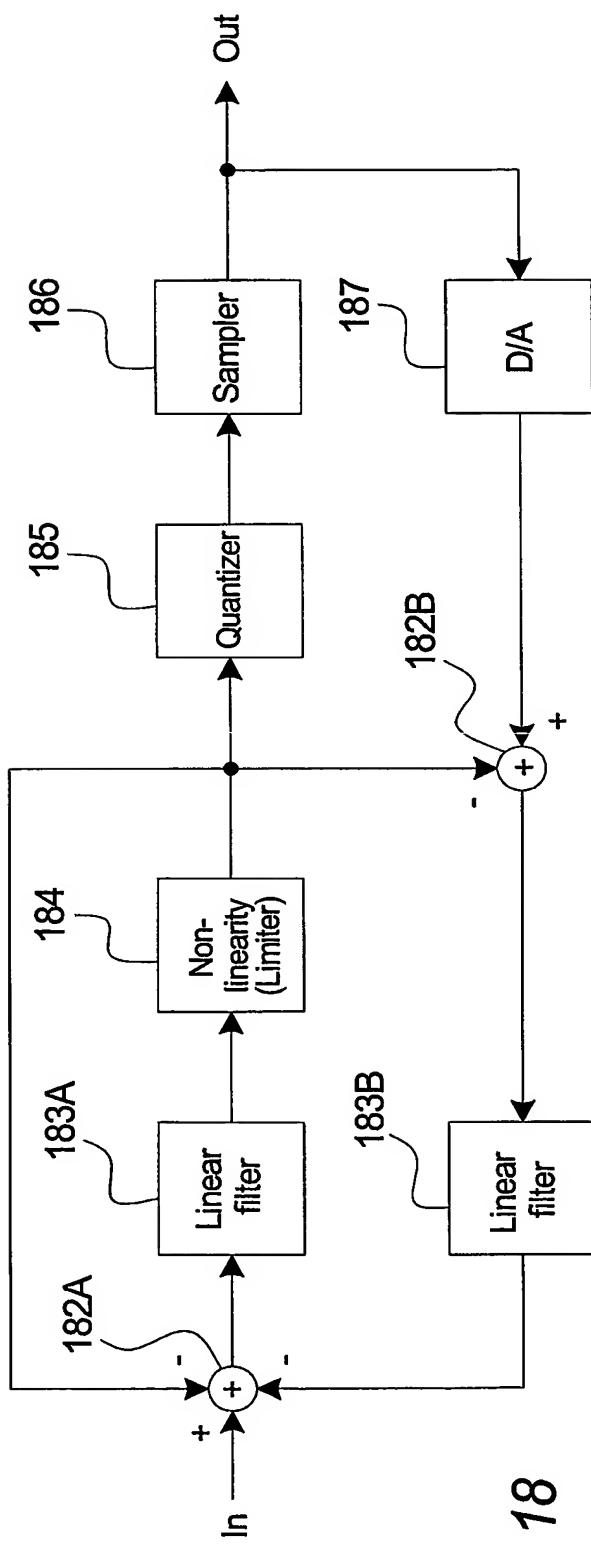


Fig. 18

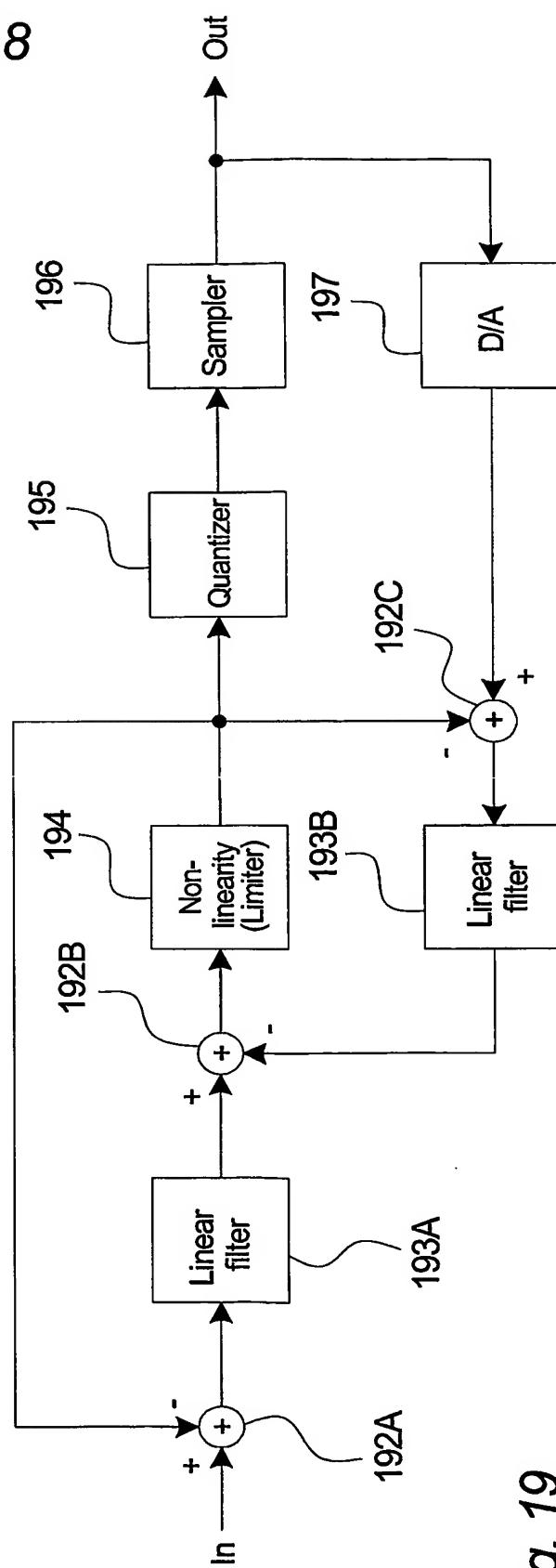


Fig. 19

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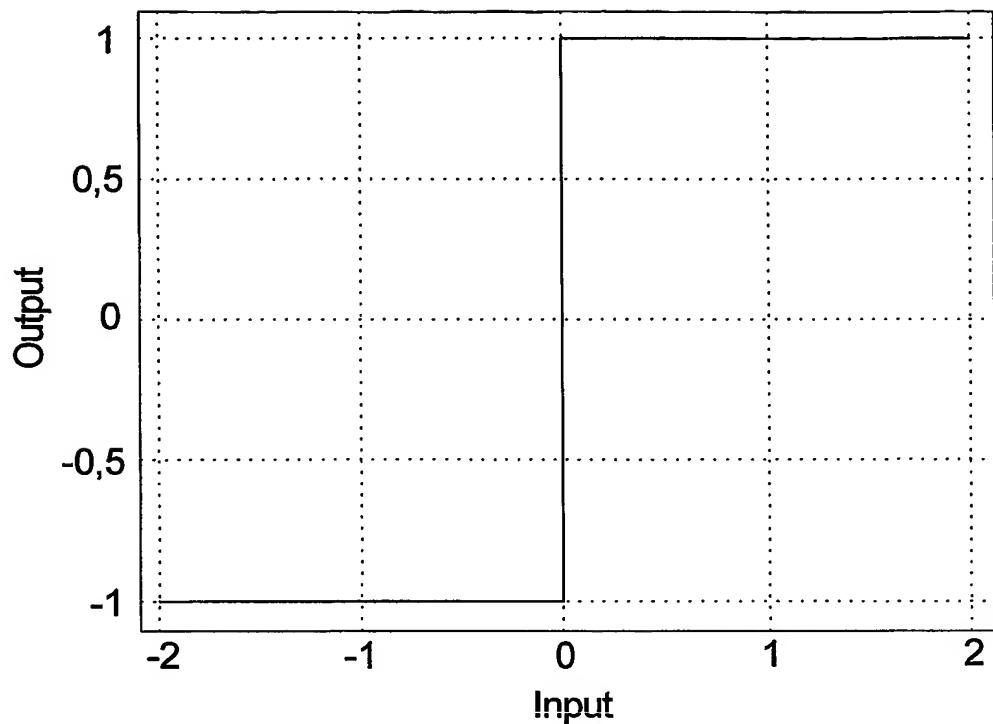


Fig. 20

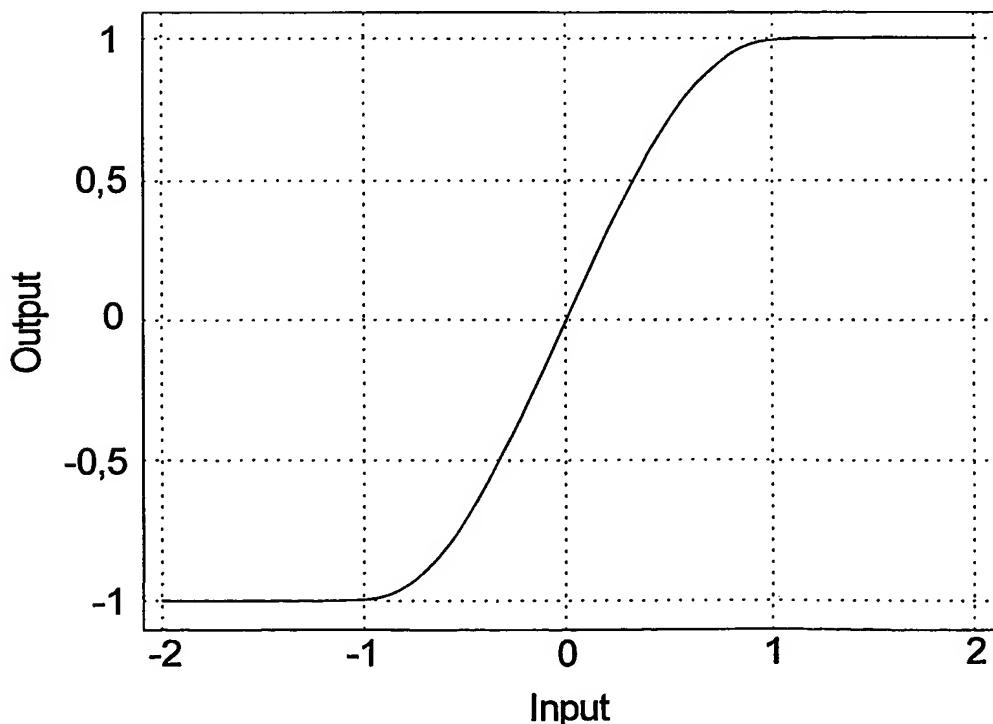


Fig. 21

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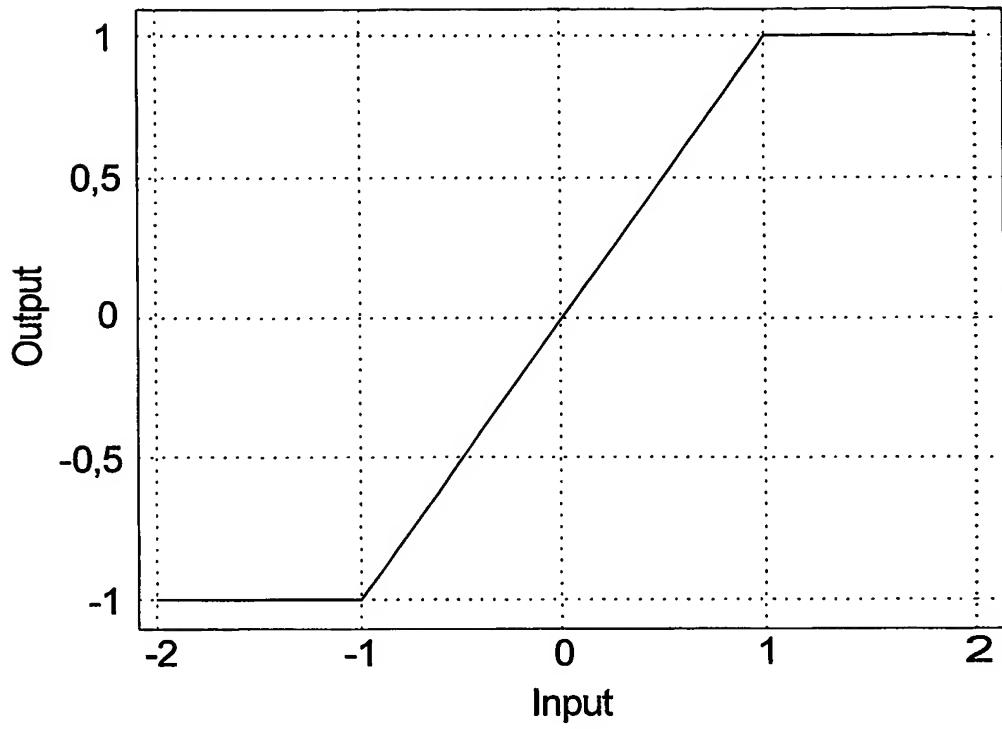


Fig. 22

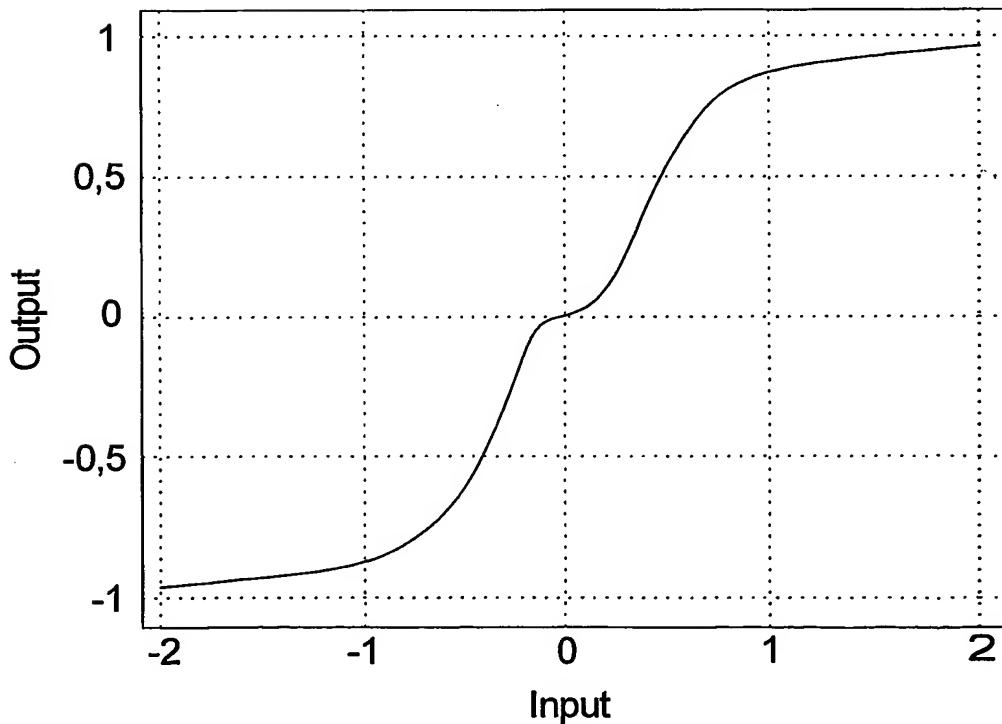


Fig. 23

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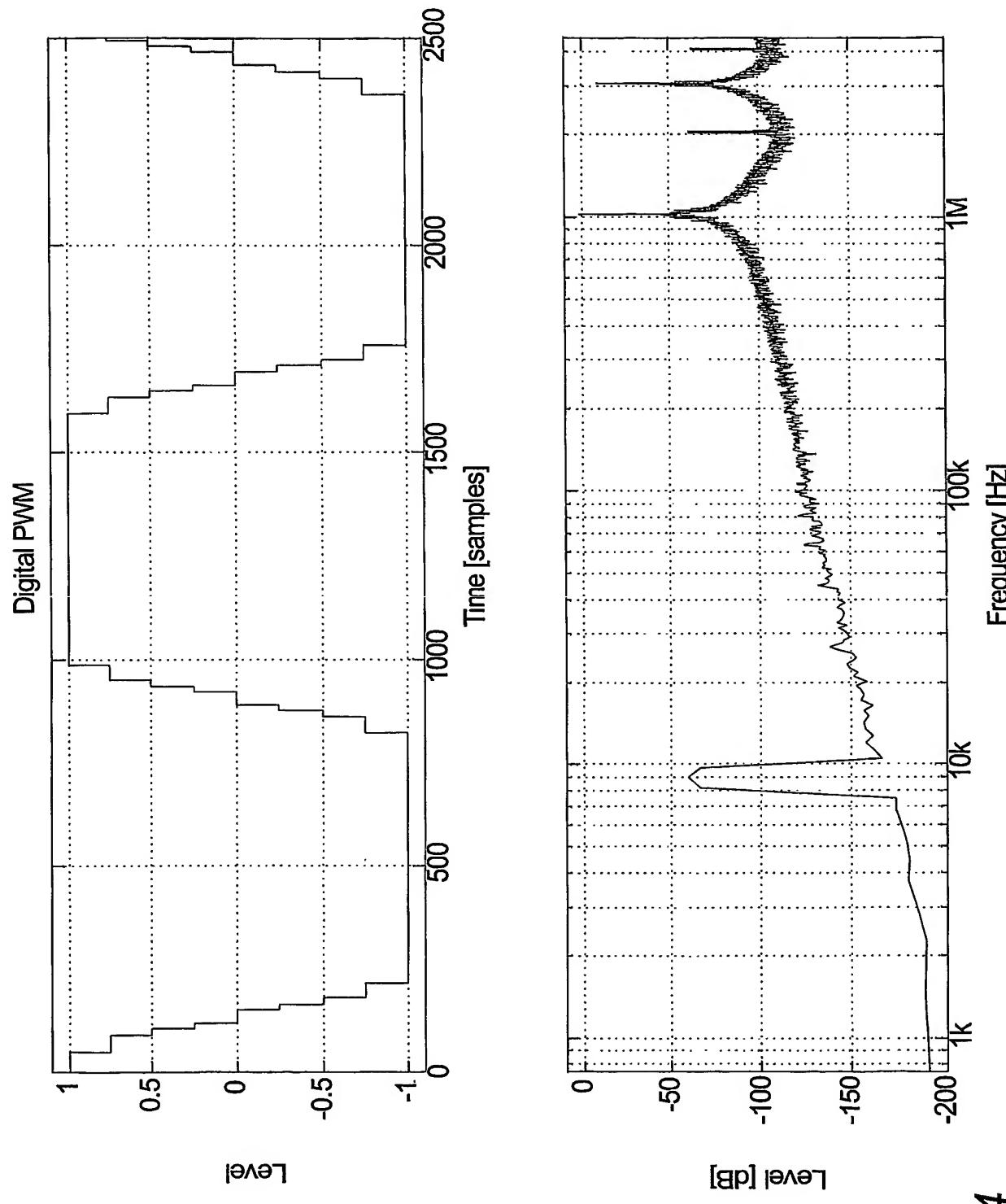


Fig. 24

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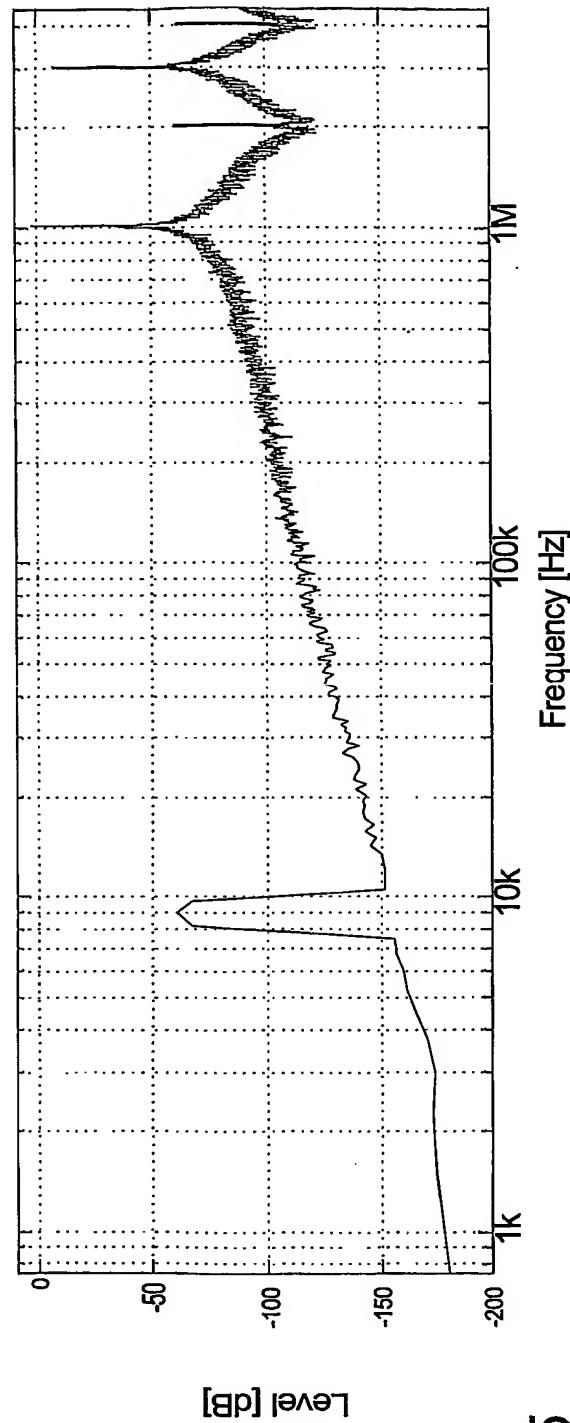
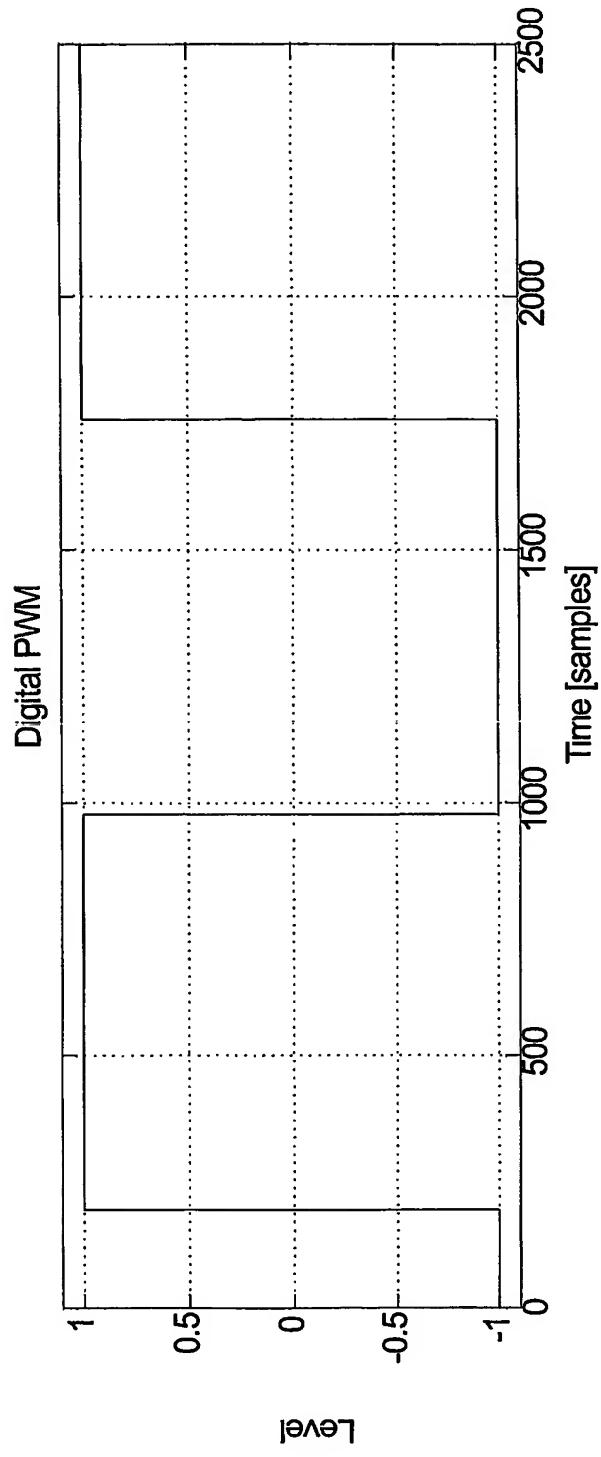


Fig. 25